SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)
(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A+' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE

M.Tech I Year I Semester (R25) Regular Examinations January – 2026
TIMINGS: 09:30 AM to 12:30 PM

DATE/DAY	POWER ELECTRONICS	COMPUTER SCIENCE AND ENGINEERING
27-01-2026 (TUESDAY)	25EE2101 Switched Mode Power Converters	25CS5801 Advanced Data Structures & Algorithms
29-01-2026 (THURSDAY)	25EE2102 Machine Modelling and Analysis	25CS5802 Distributed Operating Systems
31-01-2026 (SATURDAY)	25EE2103 DC Drives	25CS5805 Applied Machine Learning
02-02-2026 (MONDAY)	25EE2108 Smart Grid Technologies	25CS5808 Computing for Data Analytics
04-02-2026 (WEDNESDAY)	25ME3111 Research Methodology and IPR	
06-02-2026 (FRIDAY)	25EE2111 AI Techniques in Electrical Engineering	****

Note: If any omissions or clashes in this time table may please be informed to the undersigned immediately.

CONTROLLER OF EXAMINATIONS

PRINCIPAL